

IN THE CLAIMS

Please amend the claims as follows. This claim set is to replace all prior versions.

1. (currently amended) An integrated circuit device comprising:
 - a substrate;
 - a first conductive electrode on the substrate, the first conductive electrode having an electrode wall extending away from the substrate;
 - an insulating spacer on the electrode wall wherein portions of the electrode wall are free of the insulating spacer between the substrate and the insulating spacer, and wherein portions of the electrode most distant from the substrate are free of the insulating spacer;
 - a capacitor dielectric layer on portions of the ~~first conductive electrode wall~~ free of the spacer between the substrate and the insulating spacer; and
 - a second conductive electrode on the capacitor dielectric layer opposite the ~~first conductive electrode wall~~ between the substrate and the insulating spacer, wherein a thickness of the insulating spacer between the first and second conductive electrodes is greater than a thickness of the capacitor dielectric layer between the first and second conductive electrodes.
2. (original) An integrated circuit device according to Claim 1 wherein the capacitor dielectric layer and the insulating spacer comprise different materials.
3. (original) An integrated circuit device according to Claim 1 further comprising:
 - a transistor on the substrate;
 - an insulating layer between the substrate and the first conductive electrode; and
 - a conductive plug providing electrical coupling between the first conductive electrode and a source/drain region of the transistor through the insulating layer.
4. (original) An integrated circuit device according to Claim 1 wherein portions of the electrode wall extend beyond the spacer away from the substrate free of the insulating spacer, and wherein the capacitor dielectric layer is also on portions of the electrode wall extending beyond the spacer.

5. (original) An integrated circuit device according to Claim 1 wherein the electrode wall includes a recessed portion and wherein the insulating spacer is on the recessed portion of the electrode wall.

Claims 6-10 (canceled).

11. (currently amended) An electronic device comprising:

a substrate;

a conductive electrode on the substrate, the conductive electrode having an electrode wall extending away from the substrate; and

an insulating spacer on the electrode wall wherein portions of the electrode wall are free of the insulating spacer between the substrate and the insulating spacer, wherein portions of the electrode wall are exposed between the substrate and the insulating spacer, and wherein portions of the electrode most distant from the substrate are free of the insulating spacer.

12. (original) An electronic device according to Claim 11 wherein portions of the electrode wall extend from the insulating spacer away from the substrate free of the insulating spacer.

13. (original) An electronic device according to Claim 11 wherein the electrode wall includes a recessed portion and wherein the insulating spacer is on the recessed portion of the electrode wall.

14. (original) An electronic device according to Claim 11 wherein the electrode wall is closed thereby defining an inside of the electrode wall and an outside of the electrode wall.

15. (original) An electronic device according to Claim 14 wherein the electrode wall defines a cylinder.

Claims 16-18 (canceled).

19. (original) An electronic device according to Claim 11 wherein the substrate includes a transistor and wherein the conductive electrode is electrically coupled with a source/drain region of the transistor.

20. (currently amended) ~~An electronic device according to Claim 11 further comprising:~~
An electronic device comprising:

a substrate;

a conductive electrode on the substrate, the conductive electrode having an electrode wall extending away from the substrate;

an insulating spacer on the electrode wall wherein portions of the electrode wall are free of the insulating spacer between the substrate and the insulating spacer; and

a sacrificial layer on the substrate and on portions of the electrode wall free of the insulating spacer between the substrate and the insulating spacer, wherein the sacrificial layer has a thickness on the substrate such that the sacrificial layer extends to the insulating spacer, and wherein the sacrificial layer and the insulating spacer comprise different materials, wherein portions of the insulating spacer are free of the sacrificial layer, wherein the sacrificial layer extends a first distance from the electrode wall parallel to a surface of the substrate, wherein the insulating spacer extends a second distance from the electrode wall parallel to the surface of the substrate, and wherein the first distance is greater than the second distance.

Claims 21-39 (canceled).

40. (currently amended) An electronic device comprising:

a substrate; and

a conductive electrode on the substrate, the conductive electrode having an electrode wall extending away from the substrate, the electrode wall including a recessed portion at an end

thereof opposite the substrate wherein at least some of the recessed portion of the electrode wall is exposed.

41. (currently amended) An electronic device according to Claim 40 further comprising:
an insulating spacer on the recessed portion of the electrode wall wherein portions of the electrode wall are free of the insulating spacer between the substrate and the insulating spacer wherein at least some of the recessed portion of the electrode wall is exposed through the insulating spacer away from the substrate.

42. (original) An electronic device according to Claim 41 wherein portions of the electrode wall extend from the insulating spacer away from the substrate free of the insulating spacer.

43. (original) An electronic device according to Claim 40 wherein the electrode wall is closed thereby defining an inside of the electrode wall and an outside of the electrode wall.

44. (original) An electronic device according to Claim 43 wherein the electrode wall defines a cylinder.

45. (original) An electronic device according to Claim 40 further comprising:
a capacitor dielectric layer on portions of the conductive electrode.

46. (original) An electronic device according to Claim 45 further comprising:
a second conductive electrode on the capacitor dielectric layer opposite the first electrode.

47. (original) An electronic device according to Claim 46 further comprising:
an insulating spacer on the recessed portion of the electrode wall wherein portions of the electrode wall are free of the insulating spacer between the substrate and the insulating spacer

wherein the spacer has a first thickness separating the conductive electrodes, wherein the capacitor dielectric layer has a second thickness separating the conductive electrodes, and wherein the first thickness is greater than the second thickness.

48. (original) An electronic device according to Claim 40 wherein the substrate includes a transistor and wherein the conductive electrode is electrically coupled with a source/drain region of the transistor.

49. (currently amended) ~~An electronic device according to Claim 40 further comprising:~~

An electronic device comprising:

a substrate;

a conductive electrode on the substrate, the conductive electrode having an electrode wall extending away from the substrate, the electrode wall including a recessed portion at an end thereof opposite the substrate; and

a sacrificial layer on the substrate, wherein the sacrificial layer has a thickness on the substrate such that the sacrificial layer extends to the recessed portion of the electrode wall, wherein the recessed portion of the electrode wall extends beyond the sacrificial layer, and wherein the recessed portion of the electrode wall is free of the sacrificial layer.

Claims 50-67 (canceled).

68. (new) An electronic device according to Claim 20 wherein portions of the electrode wall most distant from the substrate are free of the insulating spacer.

69. (new) An electronic device according to Claim 20 wherein portions of the electrode wall extend from the insulating spacer away from the substrate free of the insulating spacer.

70. (new) An electronic device according to Claim 20 wherein the electrode wall includes a recessed portion and wherein the insulating spacer is on the recessed portion of the electrode wall.

71. (new) An electronic device according to Claim 20 wherein the electrode wall is closed thereby defining an inside of the electrode wall and an outside of the electrode wall.

72. (new) An electronic device according to Claim 71 wherein the electrode wall defines a cylinder.

73. (new) An electronic device according to Claim 20 wherein the sacrificial layer comprises a layer of an insulating material.

74. (new) An electronic device according to Claim 49 further comprising:
an insulating spacer on the recessed portion of the electrode wall wherein portions of the electrode wall are free of the insulating spacer between the substrate and the insulating spacer.

75. (new) An electronic device according to Claim 74 wherein at least some of the recessed portion of the electrode wall is exposed through the insulating spacer away from the substrate.

76. (new) An electronic device according to Claim 74 wherein portions of the electrode wall extend from the insulating spacer away from the substrate free of the insulating spacer.

77. (new) An electronic device according to Claim 49 wherein the electrode wall is closed thereby defining an inside of the electrode wall and an outside of the electrode wall.

78. (new) An electronic device according to Claim 77 wherein the electrode wall defines a cylinder.

79. (new) An electronic device according to Claim 49 wherein the substrate includes a transistor and wherein the conductive electrode is electrically coupled with a source/drain region of the transistor.

80. (new) An electronic device according to Claim 1 wherein the capacitor dielectric layer is directly on the portions of the electrode most distant from the substrate that are free of the insulating spacer.